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		. ~	200 HELLARK	December	r7, 20055	(Date)	
APPLICATION NO.	FILING DATE		FIRST NAMED INVE	NTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/642,416	08/15/2003		Min-hwa Ch	i	004192.P053D2	1462	
TITLE OF INVENTION: 1 CURRENTS	TRANSISTOR AND LOGIC	CIRCUIT ON T	HIN SILICON-ON	-INSULATOR WAFE	RS BASED ON GATE IND	DUCED DRAIN LEAKAGE	
APPLN. TYPE	SMALL ENTITY	ISSUE FI	EE I	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
nonprovisional	al NO \$1400			\$300	\$1700	12/08/2005	
EXAMINER		ART UN	IT (CLASS-SUBCLASS	12/12/2005 SFELE	KE2 00000001 10642416	
KANG, DONGHEE		2811		257-349000	9½ FC:1584	1400.0	
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Taiwan Semiconductor MFG Corp.				Taiwan, R.O.C.			
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	(from status indicated above MALL ENTITY status. See 3		☐ b. Applicant is	no longer claiming SMA	ALL ENTITY status. See 37 C	CFR 1.27(g)(2).	
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